

## WEST Search History

DATE: Tuesday, September 20, 2005

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
	<i>DB=JPAB; PLUR=YES; OP=ADJ</i>		
<input type="checkbox"/>	L26	L25 and delay\$	4
<input type="checkbox"/>	L25	L24 and active state	27
<input type="checkbox"/>	L24	memory and write adj2 (command\$ or signal\$)	4982
	<i>DB=PGPB,USPT; PLUR=YES; OP=ADJ</i>		
<input type="checkbox"/>	L23	memory and write adj2 (command\$ or signal\$)	45384
<input type="checkbox"/>	L22	L21 and refresh\$	43
<input type="checkbox"/>	L21	L20 and wait\$	97
<input type="checkbox"/>	L20	L19 and time	99
<input type="checkbox"/>	L19	L18 and read command	99
<input type="checkbox"/>	L18	L17 and command near3 (decod\$ or block\$)	99
<input type="checkbox"/>	L17	L11 and write command	107
<input type="checkbox"/>	L16	L11 and active state	8
<input type="checkbox"/>	L15	L14 and (latency or delay\$) near5 counter	3
<input type="checkbox"/>	L14	L13 and clock\$	54
<input type="checkbox"/>	L13	L12 and refresh\$	57
<input type="checkbox"/>	L12	L10 and write\$ near9 delay\$	113
<input type="checkbox"/>	L11	L10 and write\$ near7 delay\$	113
<input type="checkbox"/>	L10	L9 and (RAM or SRAM)	124
<input type="checkbox"/>	L9	L8 and fix\$ near2 latency	134
<input type="checkbox"/>	L8	L7 and latency	1939
<input type="checkbox"/>	L7	L6 and active	4495
<input type="checkbox"/>	L6	L5 and block\$	5992
<input type="checkbox"/>	L5	L4 and row\$ and column\$	6201
<input type="checkbox"/>	L4	L3 and write near2 (command or signal or pulse)	14861
<input type="checkbox"/>	L3	L2 and command\$	73637
<input type="checkbox"/>	L2	L1 and delay	196492
<input type="checkbox"/>	L1	memory	777341

END OF SEARCH HISTORY